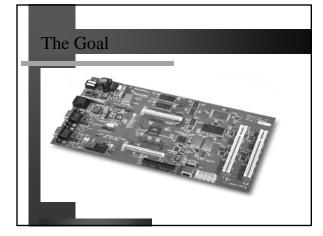
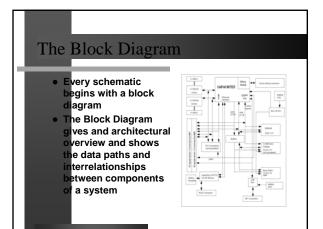


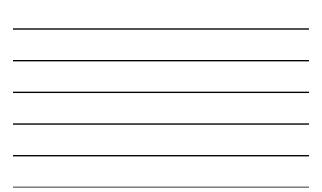
Schematic Capture

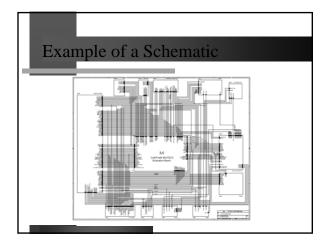
 The Schematic Capture tool is part of a suite of Computer Aided Design (CAD) tools which assist engineers in the design of modern circuits

- The CAD tool can be used to "capture" a virtual representation of a digital circuit on a computer, which can then be analyzed and simulated before an actual circuit board is created
- There are many varieties of Schematic Capture CAD tools, allowing designers to create anything from complex VLSI circuitry which can be simulated to fully, to Microprocessor based boards, which cannot!

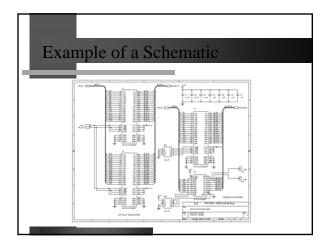




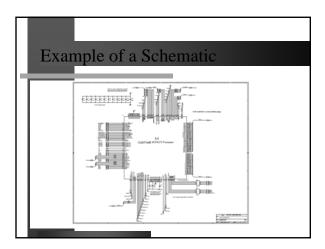




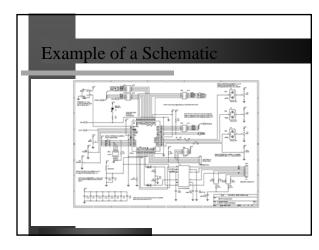




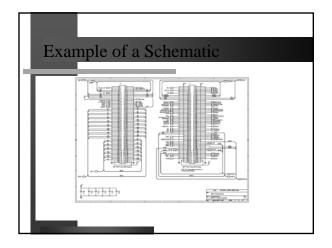




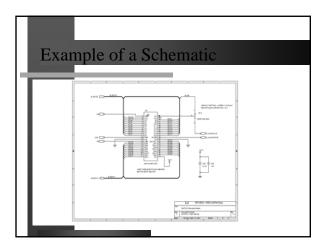




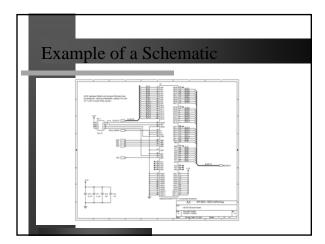




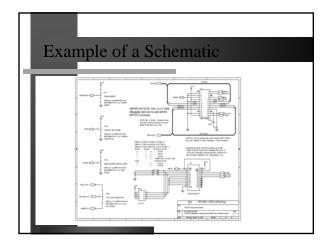


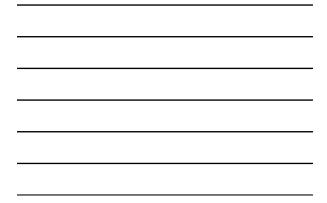


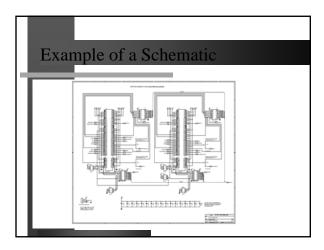




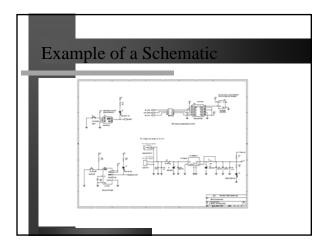




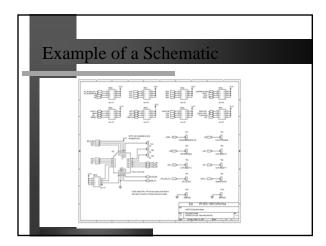




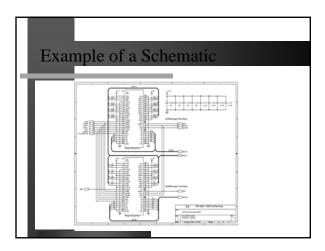




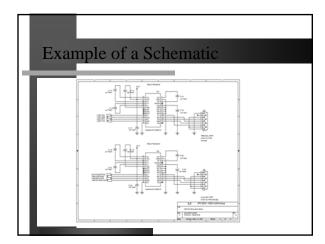




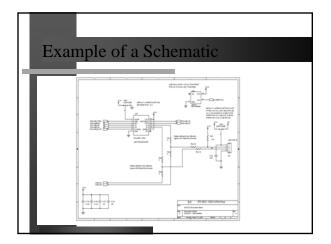




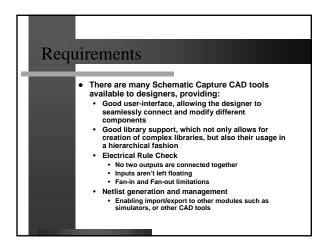












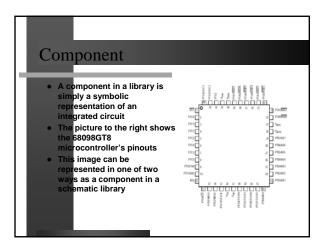
Simulation

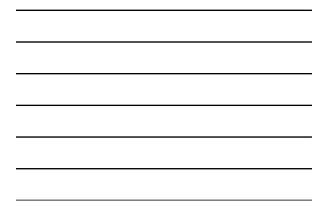
• Schematic Capture tools also allow for simulation of the electrical components

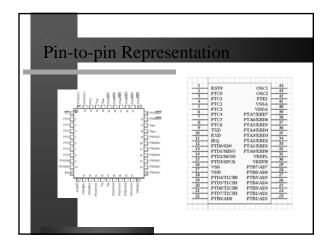
- However, with a microprocessor based design, there is no way of simulating the behavior of the circuit, in particular that of the microprocessor
- Instead, we design as carefully as possible, and hope for the best!
- Most designs go through at least one phase of redesign. This is why we first make a prototype of the system and then address all the changes in the production run of the board

Creating Libraries

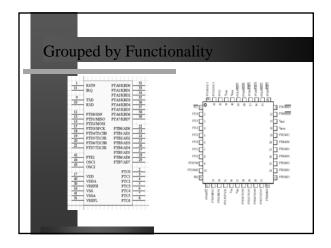
- Many CAD packages come equipped with a wide array of component libraries, which include everything from resistors and capacitors, to integrated circuits
- Chances are, however, that you will need to create a library for most of your ICs, including the microprocessor from scratch
- Creating libraries is time consuming, but of crucial importance, since it is the building blocks of your schematic
- Components in a library must be checked and rechecked to ensure pins aren't mislabeled and that there are no other errors



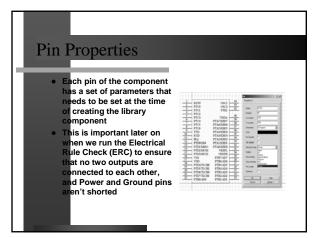


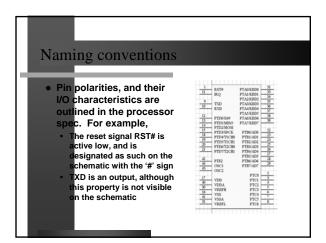


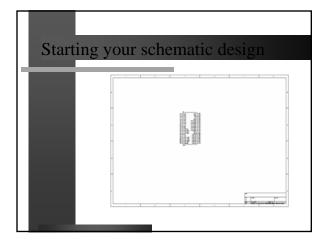


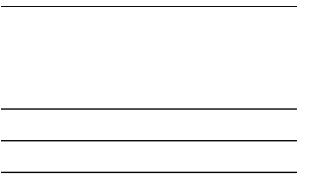


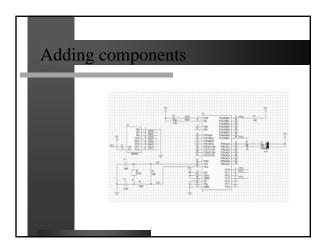








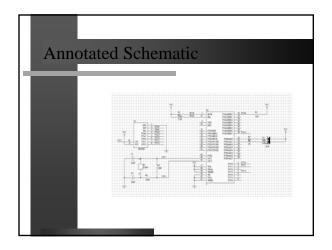


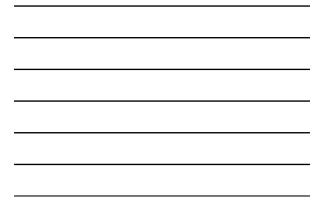




Ann	otating the Sch	ematic	
	otating the schematic assigns a		
uni	que number to every component	C?	=> C1
	ch can then be used to create a	U?	=> II1
Bill	Of Materials (BOM) which is then	R?	=> R1
	her used for manufacturing and ponent placement	R?	=> R2
RP		U?	=> 112
RP	Resistors	R?	=> R3
R C	Capacitors	R?	=> R4
, i	Inductors and ferrite beads	R?	=> R5
L D	Dindes	D?	=> D1
0	Transistor	D?	=> D2
Y	Crystal	C?	=> C2
		X?	=> X1
H	Header	R?	=> R6
JP	Jumper	R?	=> R7
5.	Connector		
RI			
	Other components		







Electrical Rule Check

- The ERC is performed to ensure that a design meets electrical rule guidelines and that a valid netlist can be generated
- The ERC checks both for electrical errors such as two output pins being connected together, and for drafting errors such as duplicate designators
- Once an ERC has completed successfully, a netlist is generated.
- A Netlist is a text file listing all the connections between components. It is used to transfer the design between different CAD tools, and also used to import the design into a PCB document

Bill of	f Materials					
The Bill of	Materials (BOM)	fact.	97	Reference	Pet	Pandon
is an order	ed list of part long with their	1	8	01.02.02.03.09.014.014.014.014 123.024.015.014.014.014.014.014 007.014.010.017.014.017.01 5.017.014.010.014.011.0 128.0104	1.0	SAT Desixaling Casacitors
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		4	1	G21	10471947	SMT Capacitor
board itsel	f, casing, etc.	4	2	622,028	275# 000 ar NPO any	SMT Case/fum
		9	3	682,691,682	1001/2017	DAT Capacitors
 It includes 	but is not limited	7	9	694	Rubycon 1000uP 30V	SMT Capacitor
to manufac	turer's part		2	CHLCH	AR PREDMOKN	SMT Capecitors
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		1	2	GL01.04	LITERO PER LISON	Her MUD
notes for e	ach component	đ	5	67,94	Molorsa MBRS34073	SAT Schotty Power Distes
		13	1	CP	L07670 0 test LED	Hiteer SVT UED